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A28838-I-A  
PATENT ✓ 4

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JUN 15 2004  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hidemi Takasu

Serial No.: 09/212,915

Examiner: M. Estrada

Filed : December 16, 1998

Group Art Unit: 2823

For : PROCESS FOR FABRICATING A  
SEMICONDUCTOR DRIVE

RESPONSE TO OFFICE ACTION

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box AF, P.O. Box 1450, Alexandria, VA 22313-1450, on:

June 3, 2004

Date of Deposit

Michael McNamara

Attorney Name

Signature

52,017

PTO Reg. No.

June 3, 2004

Date of Signature

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Examiner's Action mailed December 3, 2003.

Claim Listing:

1. (Currently Pending) A process for fabricating a semiconductor device having a buried layer comprising the steps of:

forming a buried implanted impurity ion region at a location which is